

In re Applicant:

Subhasish Mitra et al.

Art Unit:

2117

Serial No.:

10/656,013

Examiner:

Phung M. Chung

Filed:

September 5, 2003

Docket:

ITL.0971US

P16171

For:

**Compacting Circuit** 

Responses

Assignee:

**Intel Corporation** 

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## **REPLY TO OFFICE ACTION**

Sir:

In response to the office action mailed May 14, 2007, please amend the above-referenced patent application as follows:

Date of Deposit: August 1, 2007

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the